

# Dr. Cédric Herzet

## Electrical Engineer

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born the 7th of April 1978  
belgian, unmarried

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### Educational Background

- 2002-2006      PhD. Student at the Communications and Remote Sensing Laboratory, Université Catholique de Louvain, Belgium.  
Supervisor : Pr. Luc Vandendorpe.  
During my thesis I addressed the problem of designing and studying efficient algorithms enabling to recover the system synchronization at low-SNRs.
- 2001-2003      DEA in Applied Science, Université Catholique de Louvain, *summa cum laude*.  
Thesis supervised by Pr. Luc Vandendorpe : *Code-aided Iterative Synchronization Algorithms*.
- 1996-2001      Electrical Engineer, Université Catholique de Louvain, *cum laude*.  
Thesis supervised by Pr. Luc Vandendorpe : *Turbo réception pour une liaison montante UMTS-WCDMA*.

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### Professional Background

- October 2006-  
October 2007      Fulbright Postdoctoral Stay at UC Berkeley, USA.  
I am currently focussing on the design of efficient algorithms for joint source-channel decoding.
- May to August  
2006      Research Assistant, ENS Cachan, Paris, France.  
I focussed on the computation of estimation lower bounds for time-varying parameters.
- 2002-2006      Research Assistant, Université Catholique de Louvain, Louvain-la-Neuve.  
I worked in the Communications and Remote Sensing Laboratory in collaboration with Professor Luc Vandendorpe. I give lectures in Electromagnetics and Digital Communications to engineering students.
- August to  
December 2001      Research Engineer, Université Catholique de Louvain, Louvain-la-Neuve.  
I designed VDSL systems in collaboration with Alcatell Bell (Antwerpen Belgium).

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### Languages

- French      Native Language.
- English      Good knowledge.
- Spanish, Dutch      Fair knowledge.

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### Centers of Interest

Above all, I like to have a very active social life. This includes doing sport, being involved in organizations and travelling as much as possible...